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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/781,518	02/17/2004	Shih-Hsiung Huang	250122-1210	2946

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EXAMINER

SHANKAR, VUAY

ART UNIT	PAPER NUMBER
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2629

DATE MAILED: 10/03/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/781,518	Applicant(s) HUANG, SHIH-HSIUNG	
	Examiner VIJAY SHANKAR	Art Unit 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 23 July 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1 and 8 is/are rejected.
- 7) ☒ Claim(s) 2-7 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1 and 8 are rejected under 35 U.S.C. 102(e) as being anticipated by Yano et al (6,999,058).

Regarding Claim 1, Yano et al teaches a liquid crystal display, comprising: a power device (Fig.3A-B); a display unit array (Fig.3A-B); a scan driver (120 in fig.3B), coupled to the power device, outputting a plurality of scan signals to the display unit array, wherein the scan driver sequentially outputs the scan signals to the display unit array in normal operation of the liquid crystal display, and the scan driver outputs an erase signal and all the scan signals during shutdown and power on processes of the liquid crystal display (see Figs. 3A-B; Col. 4, line 43- Col.5, line 67; Col.8, line 56- Col.10, line 26); a selection device having a first input terminal coupled to the power device, a first output terminal coupled to the scan driver, a second output terminal, and a first control terminal, wherein when the first control terminal receives the erase signal, and the selection device couples the first input terminal to the second output terminal (see Figs. 3A-B,4; Col. 4, line 43- Col.5, line 67; Col.8, line 56- Col.10,

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line 26); and a current limiting device (102 in fig.3A; Col.4, line43- Col.5, line 65), coupled between the second output terminal and the scan driver, limiting instantaneous current from the power device when the scan driver simultaneously outputs all the scan signals. (see Figs. 3A-B; Col. 4, line 43- Col.5, line 67; Col.8, line 56- Col.10, line 26).

Regarding Claim 8, Yano et al teaches a liquid crystal display wherein the current limiting device is a resistor (102 in fig.3A).

Allowable Subject Matter

3. Claims 2-7 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

4. The following is an examiner's statement of reasons for allowance: The prior arts fails to teach the liquid crystal display wherein the selection device comprises: a first switch, having a second input terminal coupled to the first input terminal, a second control terminal coupled to the first control terminal, and a third output terminal coupled to the first output terminal, turned on and coupling the first input terminal to the first output terminal in normal operation; and a second switch, having a third input terminal, a third control terminal coupled to the first terminal, and forth output terminal coupled to the second output terminal, turned

on according to the erase signal and coupling the first input terminal to the second output terminal during shutdown and power on processes as claimed in Claim 2.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Response to Arguments

5. Applicant's arguments filed 7-23-06 have been fully considered but they are not persuasive.

Applicant argues that Yano et al does not teach a LCD comprising a scan driver, coupled to the power device, outputting a plurality of scan signals to the display unit array, wherein the scan driver sequentially outputs the scan signals to the display unit array in normal operation of the liquid crystal display, and the scan driver outputs an erase signal and all the scan signals during shutdown and power on processes of the liquid crystal display.

However, Yano et al teaches LCD comprising a scan driver (120 in fig.3B), coupled to the power device, outputting a plurality of scan signals to the display unit array, wherein the scan driver sequentially outputs the scan signals to the display unit array in normal operation of the liquid crystal display, and the scan driver outputs an erase signal and all the scan signals during shutdown and power on processes of the liquid crystal display (see Figs. 3A-B; Col. 4, line 43- Col.5, line 67; Col.8, line 56- Col.10, line 26)

Applicant argues that Yano et al does not teach a LCD comprising a selection device having a first input terminal coupled to the power device, a first output terminal coupled to the scan driver, a second output terminal, and a first control terminal, wherein when the first control terminal receives the erase signal, and the selection device couples the first input terminal to the second output terminal.

However, Yano et al teaches a LCD comprising a selection device having a first input terminal coupled to the power device, a first output terminal coupled to the scan driver, a second output terminal, and a first control terminal, wherein when the first control terminal receives the erase signal, and the selection device couples the first input terminal to the second output terminal (see Figs. 3A-B,4; Col. 4, line 43- Col.5, line 67; Col.8, line 56- Col.10, line 26)

Conclusion

6. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571) 272-7682. The examiner can normally be reached on M-F 7:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BIPIN SHALWALA can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to read 'Vijay Shankar', with a stylized, cursive script.

VIJAY SHANKAR
Primary Examiner
Art Unit 2629